

Network Working Group
Request for Comments: 12 REVISED

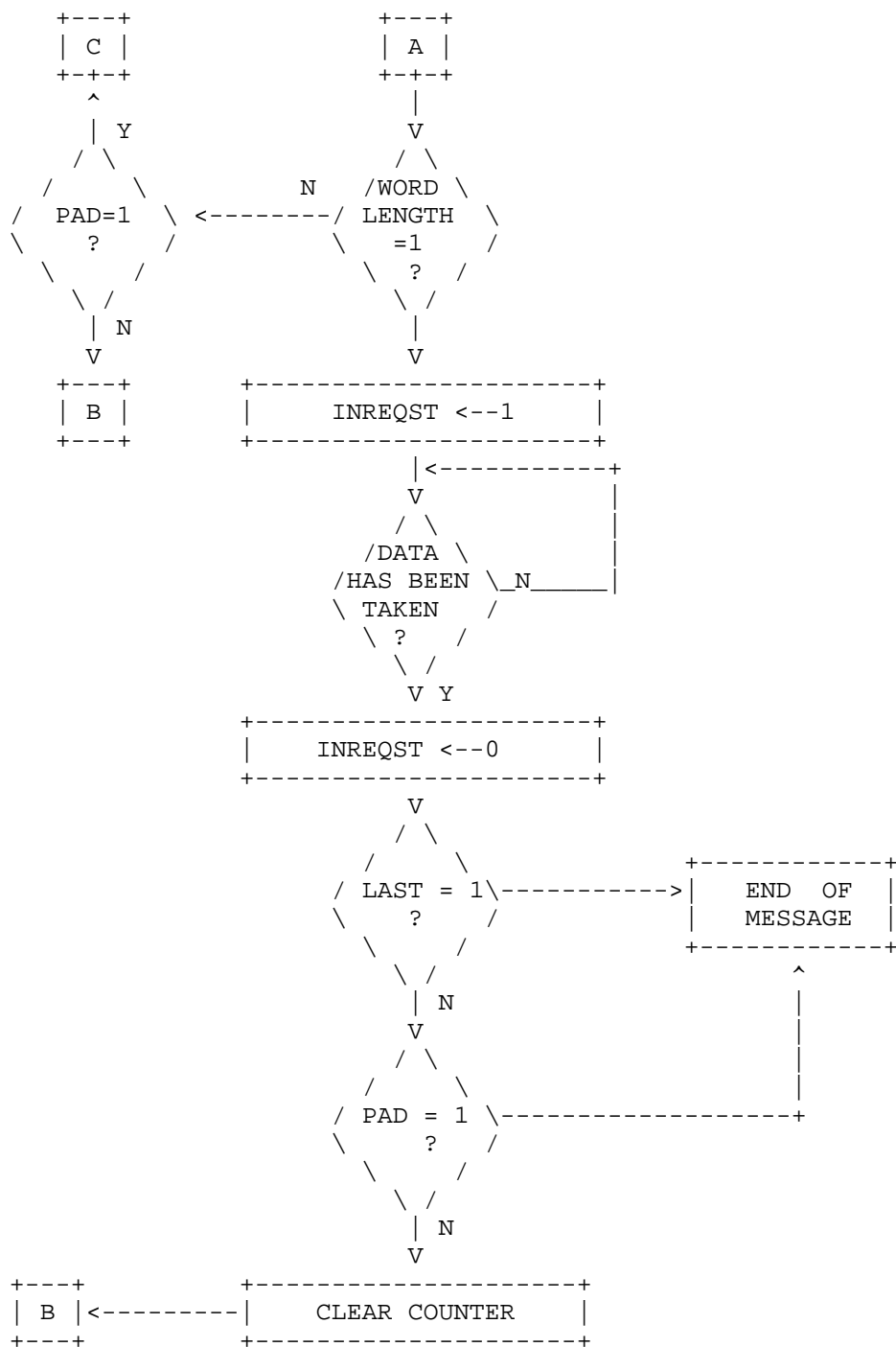
M. Wingfield
26 August 1969

IMP-HOST INTERFACE FLOW DIAGRAMS

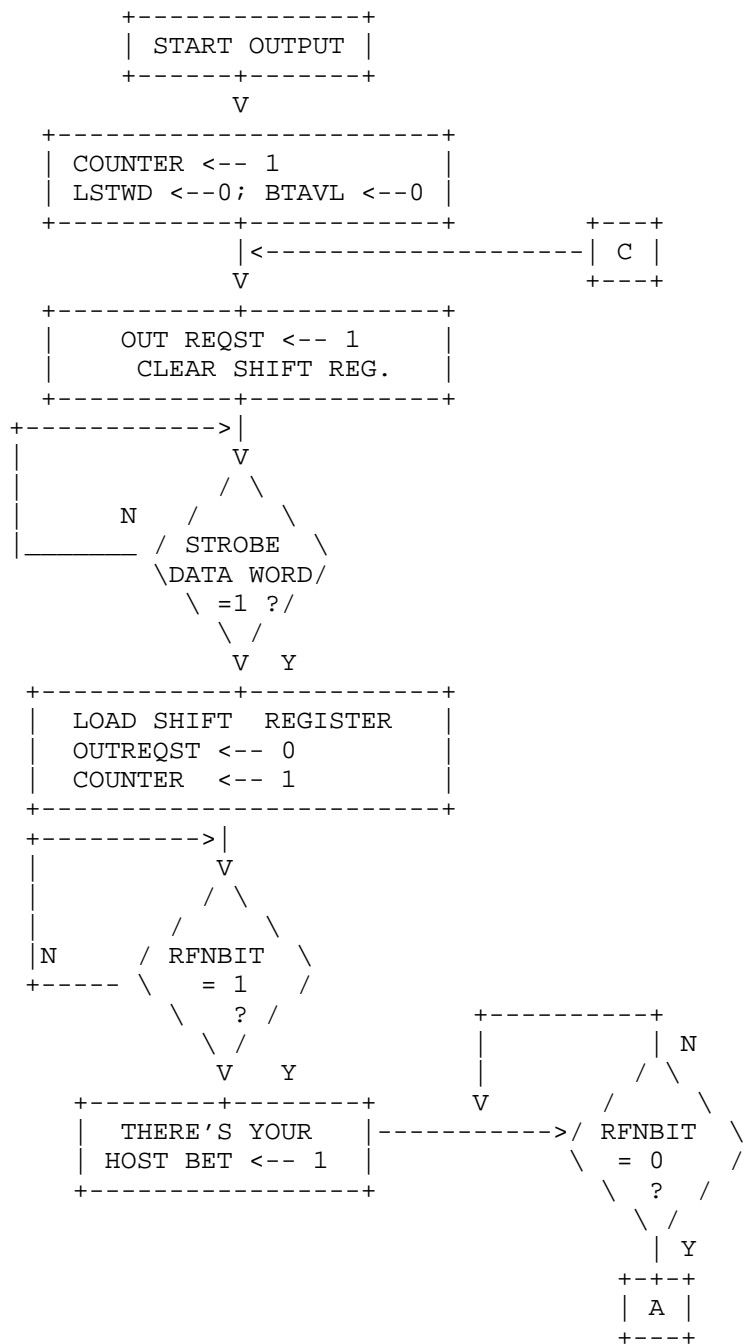
The following flow diagrams were extracted from the logic diagrams provided in Appendix B of BBN Report No. 1822. These diagrams indicate the logical sequence of hardware operations which occur within the IMP-HOST interface. The logic names appearing in the blocks correspond to the logic elements found in Appendix B.

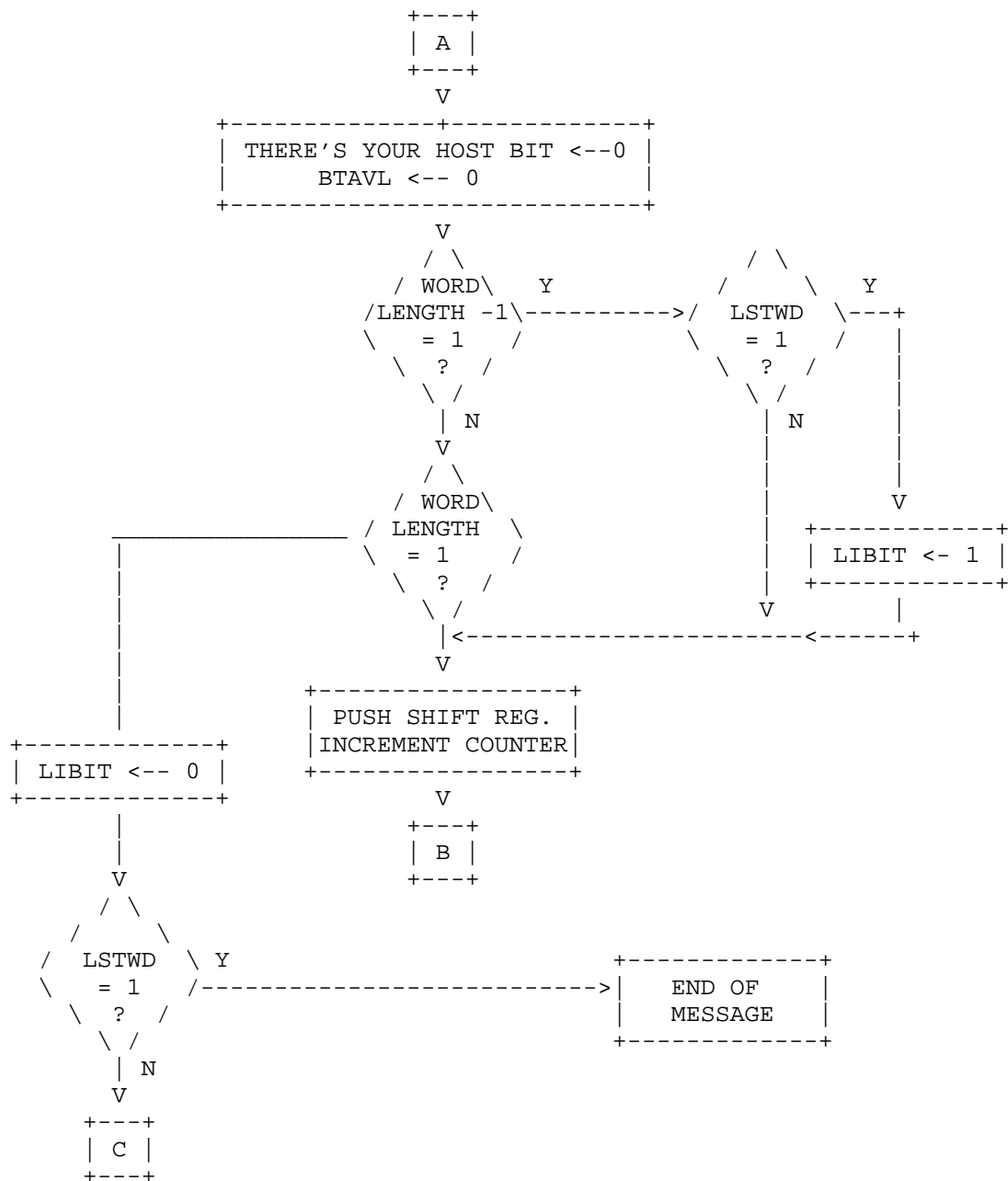
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graph TD
    Start([Start Input]) -- V --> Init[| LAST <--0; IMPERR <--0 |  
| PAD <--0; IN REQST <--0 |  
| COUNTER <--0 |]
    Init -- V --> RFNIB[| RFNIB <-- 1 |]
    RFNIB -- V --> J1(( ))
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HOST TO IMP MESSAGE





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